

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Andrew G. Haerle, et al.

Title: METHOD FOR TREATING SEMICONDUCTOR PROCESSING
COMPONENTS AND COMPONENTS FORMED THEREBY

App. No.: Divisional of U.S. Serial No.: 10/414,563 Filed: HERewith

Atty. Dkt. No.: 1075-E4283-DIV

MS PATENT APPLICATION

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the first action on the merits, please amend the above-identified application as follows: